

PCB Laminate Specification for PCB 400921

Layer Stack Legend

Layer	Material	Thickness	Dielectric Material	Type	Gerber	Comment
Top Overlay				Legend	GTO	
Top Solder	Surface Material	0.030mm	Solder Resist Halogen-Free	Solder Mask	GTS	Follow standard process
Top Surface Finish	Nickel, Gold	0.003mm		Surface Finish		Follow standard process
Top Layer	Copper	0.035mm		Signal	GTL	
	Prepreg	0.090mm	FR-4 Halogen-Free	Dielectric		Adjustment for impedance allowed
Mid-Layer 1	Copper	0.035mm		Signal	G1	
	Core	0.430mm	FR-4 Halogen-Free	Dielectric		Adjustment to match final thickness allowed
Mid-Layer 2	Copper	0.035mm		Signal	G2	
	Prepreg	0.090mm	FR-4 Halogen-Free	Dielectric		Adjustment for impedance allowed
Bottom Layer	Copper	0.035mm		Signal	GBL	
Bottom Surface Finish	Nickel, Gold	0.003mm		Surface Finish		Follow standard process
Bottom Solder	Surface Material	0.030mm	Solder Resist Halogen-Free	Solder Mask	GBS	Follow standard process
Bottom Overlay				Legend	GBO	
Total thickness: 0.817mm						

PCB Details

PCB Supplier	Suppliers Choice
Number of copper layers	4
Laminate Material	FR-4
Total Board Thickness	0.8mm +/- 10%
Copper thickness	See figure above
PCB Stackup	See figure above
Solder Mask Color	Blue
Silkscreen Color	White
Surface Finish	ENIG Gold
Compliance Enviromental directives	RoHS, REACH, Halogen-Free
Via protection	IPC-4761 type I-b (Tented)

Gerber file extension convention conforming to general industry practice

.GTL	Top Layer
.GBL	Bottom Layer
.G1	Mid-Layer 1
.G2	Mid-Layer 2
.GTO	Top Overlay
.GBO	Bottom Overlay
.GTP	Top Paste Mask
.GBP	Bottom Paste Mask
.GTS	Top Solder Mask
.GBS	Bottom Solder Mask
.GM1	Mechanical 1 (Used for board outline)
.GM7	Mechanical 7 (Used for top assembly)
.GM8	Mechanical 8 (Used for bottom assembly)
.GM11	Mechanical 11 (Used for top layer tracks impedance control)

Drill Table

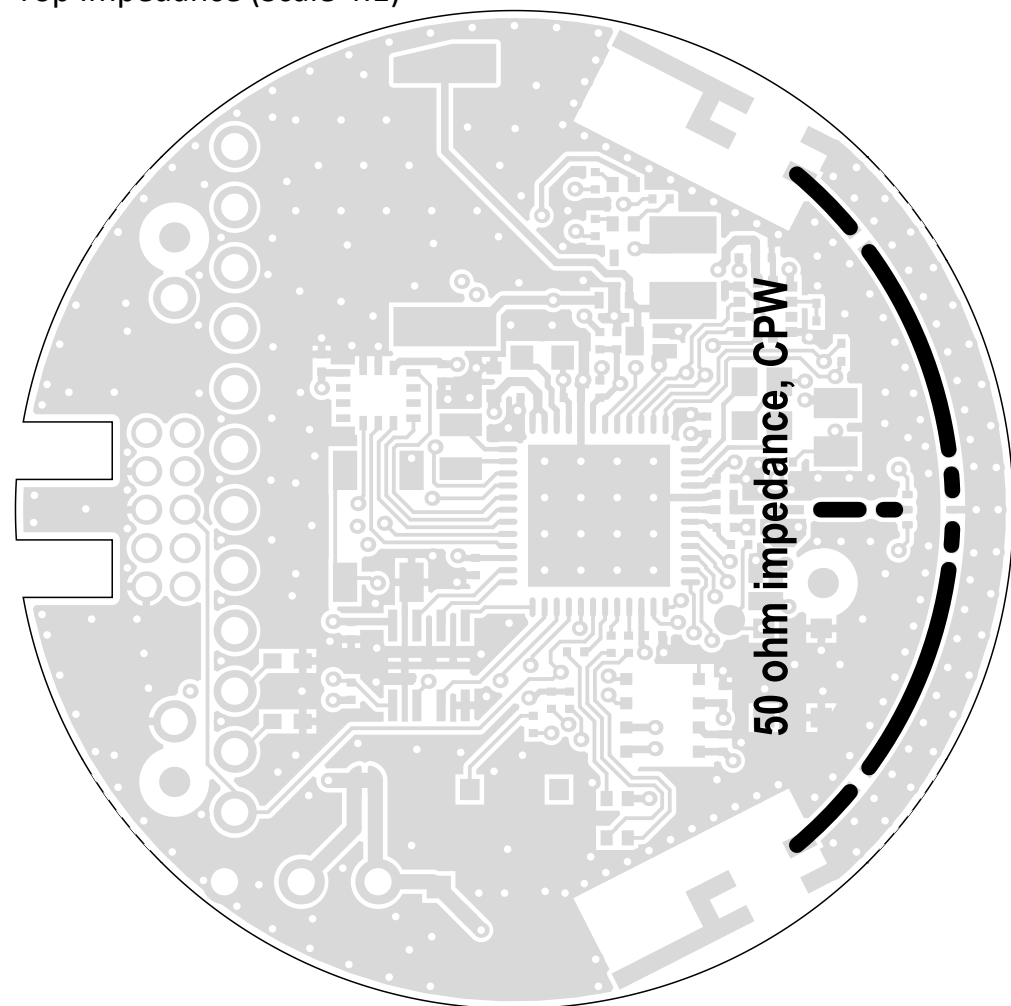
Symbol	Count	Hole Size	Plated	Hole Tolerance
◇	368	0.250mm	Plated	
▣	20	0.400mm	Non-Plated	
⊠	10	0.700mm	Plated	
◎	17	0.900mm	Plated	
	415 Total			

Title nRF54L15 Tag		
Size: A3	Part Number: 400921	Revision: 0491RUBR
Date: 2026-04-15		Sheet 1 of 2
File: 400921.PCBDwf		Drawn By: RUBR
Classification: PUBLIC		

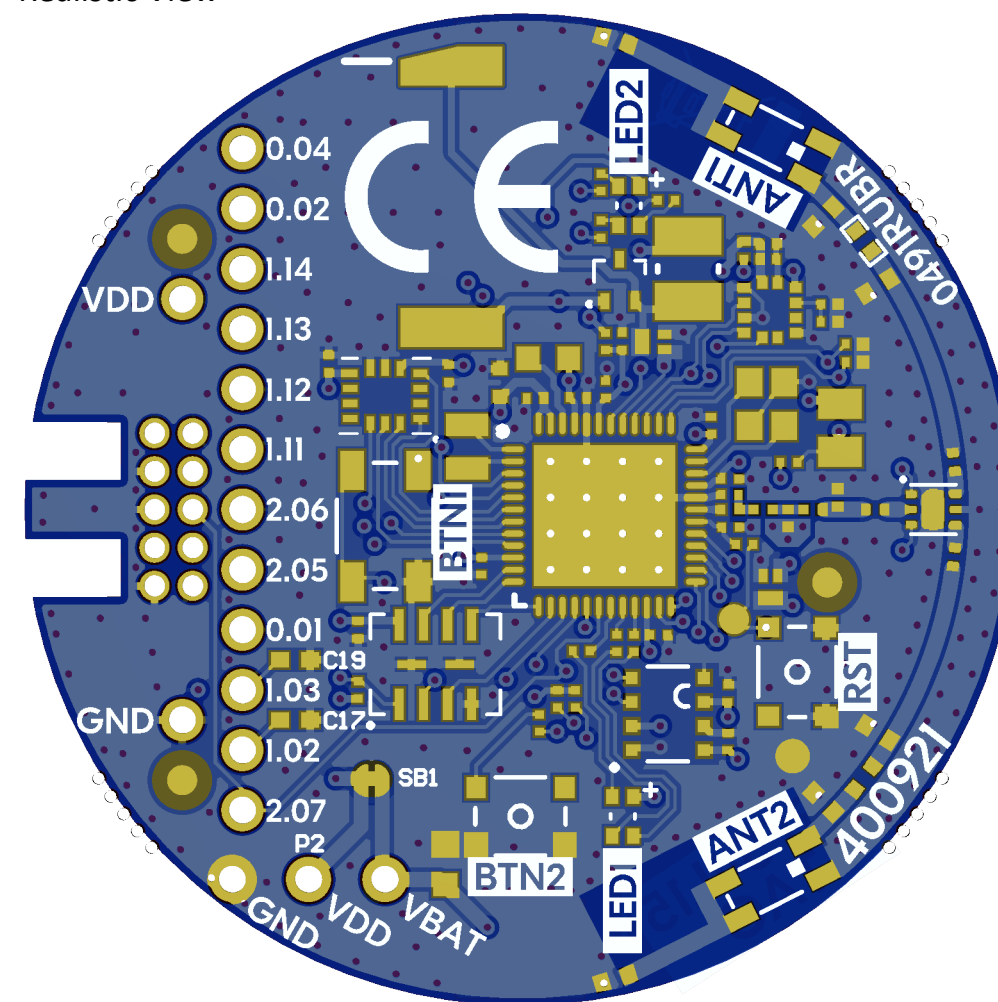



PCB Information for PCB 400921

Top Impedance (Scale 4:1)



Realistic View



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